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IBM Research - Zurich

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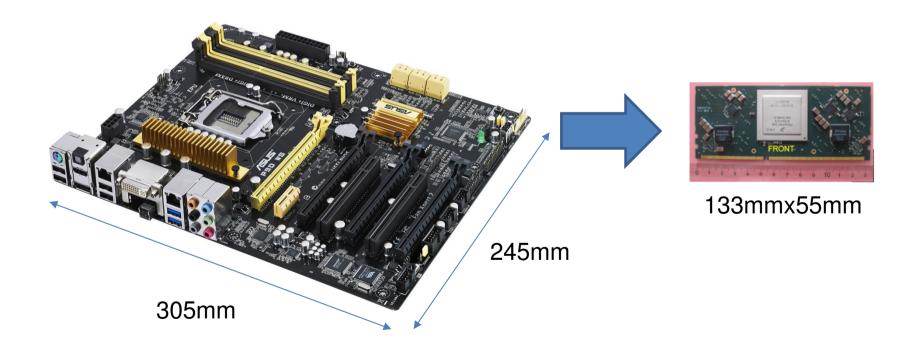
DISCLAIMER: This presentation is entirely Ronald's view and not necessarily that of IBM.

Rules

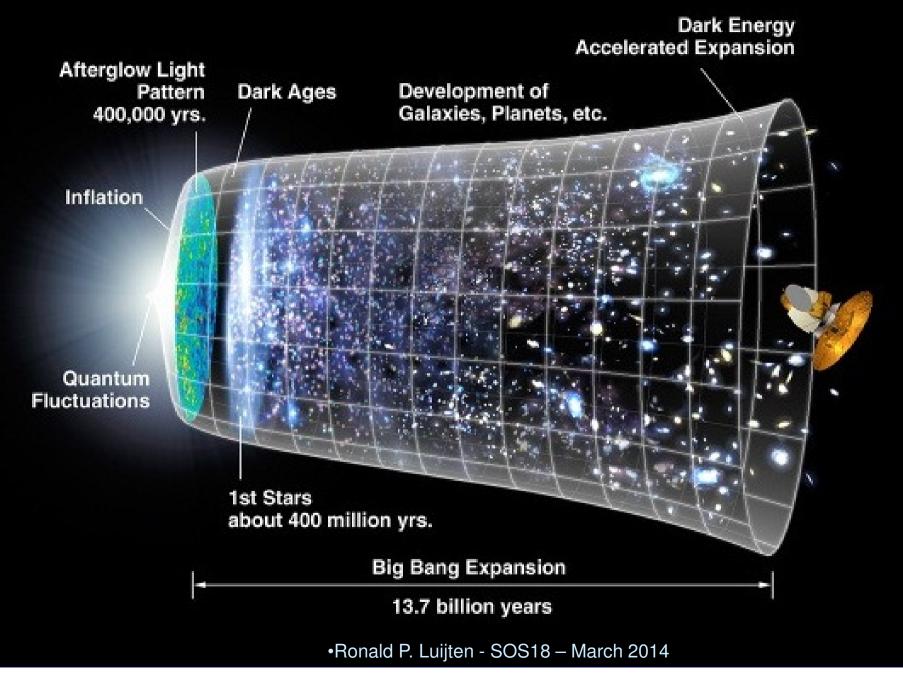
Definition

μServer:

The integration of the entire server node motherboard (no graphics) into a single microchip except DRAM, Nor-boot flash and power conversion logic.



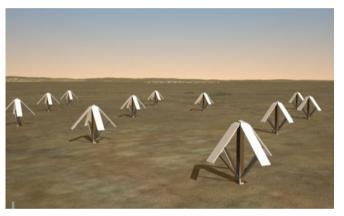
SKA (Square Kilometer Array) to measure Big Bang







SKA: Largest Radio-astronomy antenna Big data on Steroids

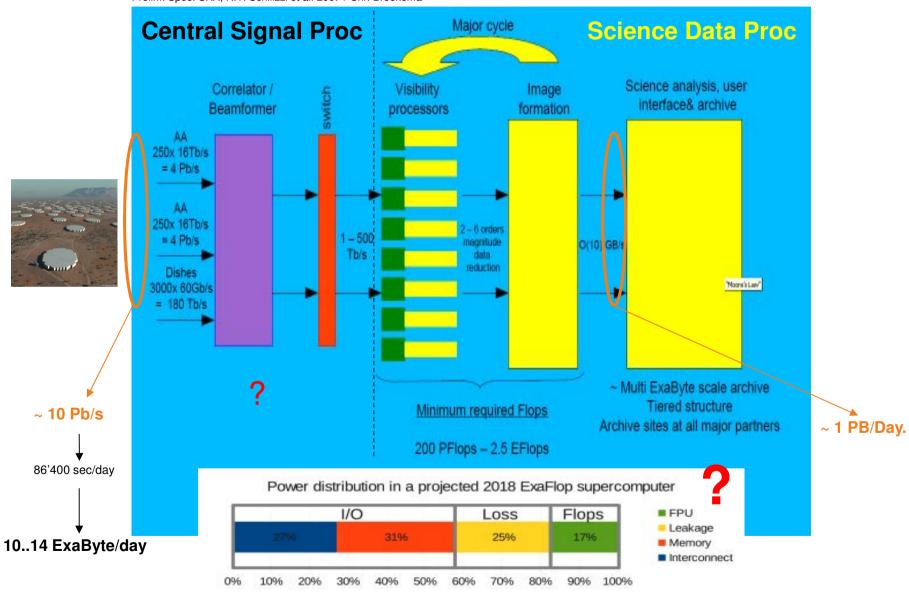






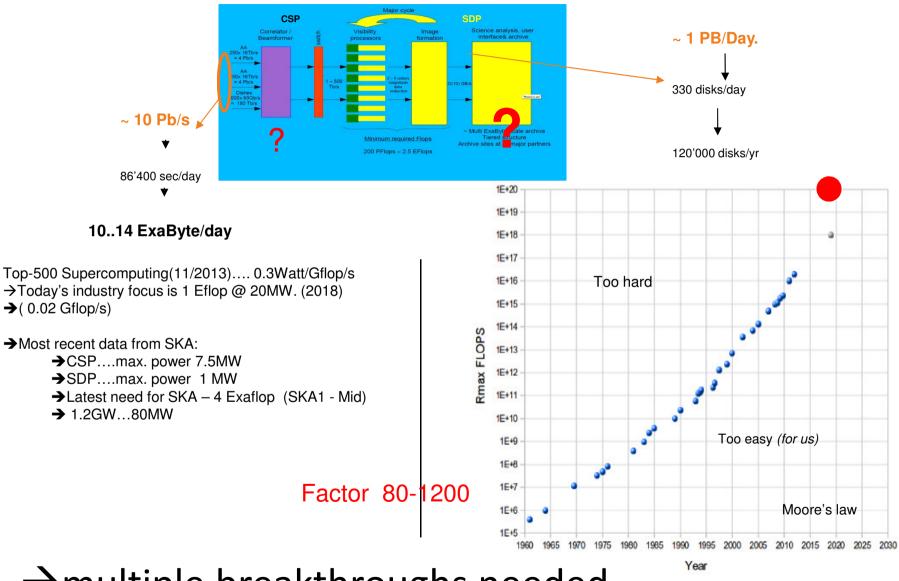
Up to 2 Million+ Antenna's What does this mean?





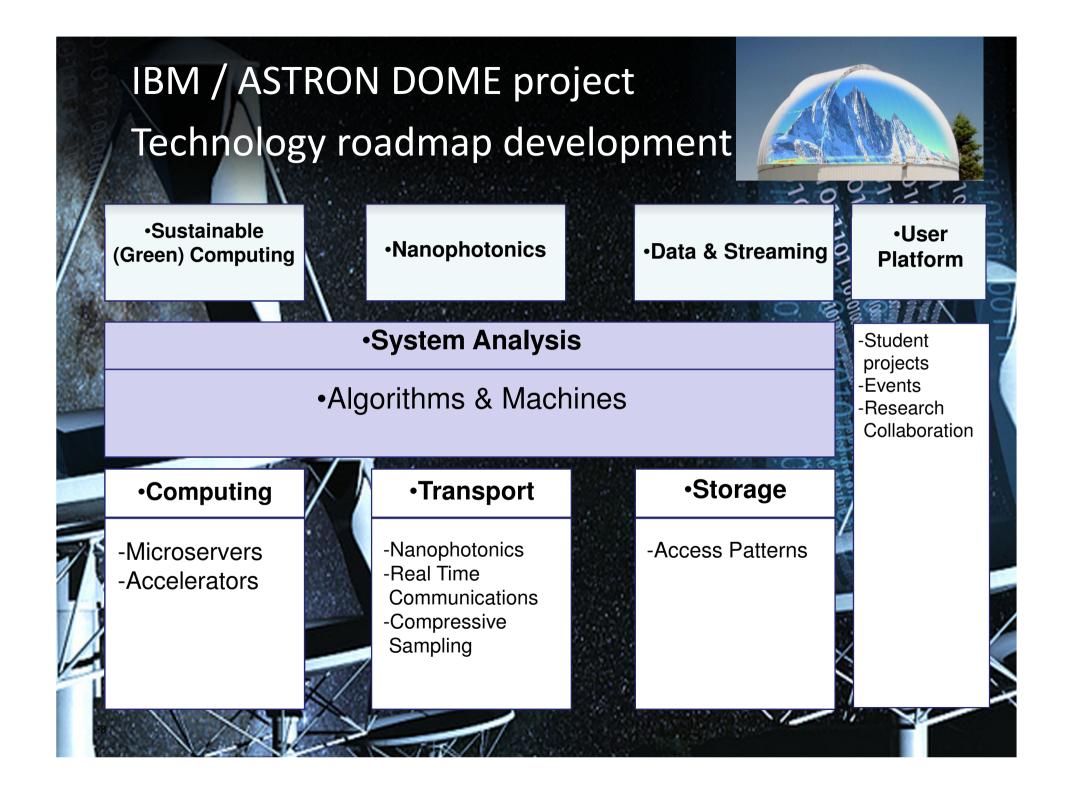
Prelim. Spec. SKA, R.T. Schilizzi et al. 2007 / Chr. Broekema





 \rightarrow multiple breakthroughs needed

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AST(RON IBM DOME µServer Motivation & Objectives



• Create the worlds highest density 64 bit μ-server drawer

- Useful for both SKA radio-astronomy and IBM future business
 - Platform for Business Analytics appliance pre-product research
 - "Datacenter in-a-box"
- Very high energy efficiency / very low cost (radioastroners...)
- Use commodity components only, HW + SW standards
- Leverage 'free computing' paradigm
- Enhance with 'Value Add': packaging, system integration, ...
- speed of light

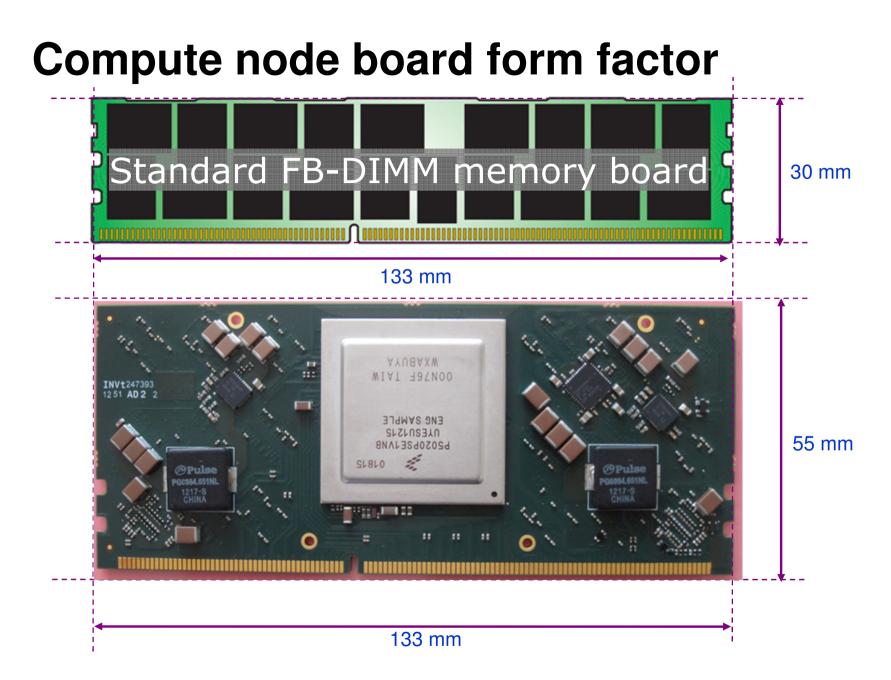


Must be true 64 bit to enable business applications

- Must run server class OS (SLES11 or RHEL6, or equivalent)
 - Currently precludes ARM (64-bit Silicon not yet available)
 - PPC64 is available in SoC from FSL now
 - (I am poor no \$\$\$ for my own SoC...)
- This is a research project capability demonstrator only







•Ronald P. Luijten - SOS18 - March 2014

Compute node processor options

FSL SoC parts	P5040	T4240
CPU GHz	2.2	1.8
CPUs	4 cores, 1 thread per core	12 cores, 2 threads per core
Primary cache	32 KB I + 32 KB D per core	32 KB I + 32 KB D per core
Secondary cache	512 KB I+D	2 MB per 4 CPUs
L3 cache	1 MB on chip	1.5 MB on chip
Memory	2 x 2 GB, DDR3/L3, ECC	3 x 2 GB, DDR3/L3, ECC
core	e5500, ppc64	e6500, ppc64
	1 DP FP unit per core	1 DP FP unit per core 128 bit SP altivec unit per core
node	45nm	28nm
TDP	55W	60W

T4240 DIMM connector:

- •2 times SATA
- •4 times 10 Gigabit ethernet
- •SD card interface
- •USB interface
- •Some power supplies

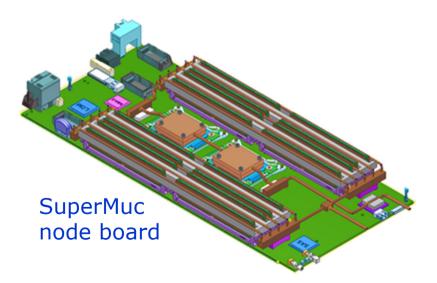
Hot Water Cooling

Most Energy Efficient solution:

- Low PUE possible (<=1.1) Green IT
- 40% less energy consumption compared to air-cooled systems
- 90% of waste heat can be reused (CO₂ neutral according Kyoto protocol)
- Allows very high density
- Less thermal cycling improved reliability
- Lower T_i reduces leakage current further saving energy

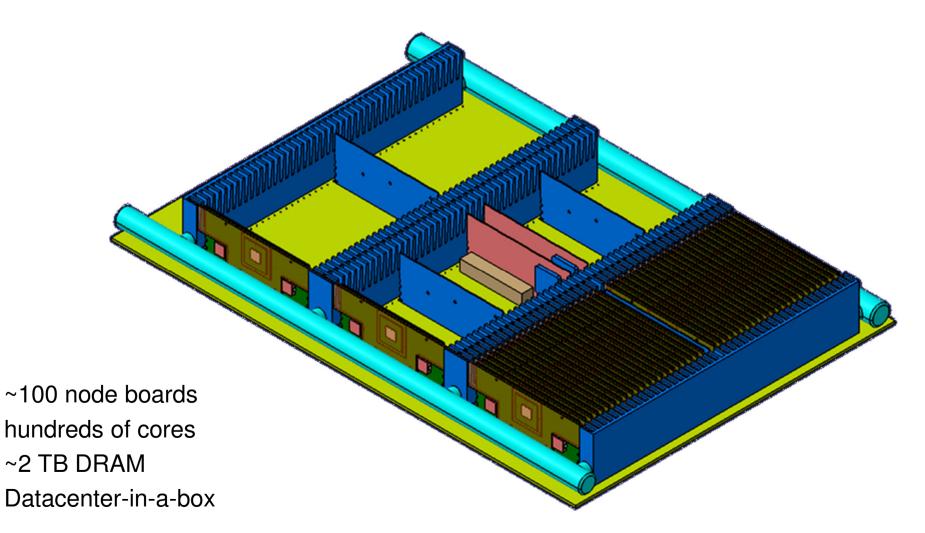
SuperMUC HPC machine at LRZ in Germany demonstrates ZRL hot water cooling

- No 4 on June 2012 TOP500 HPC list





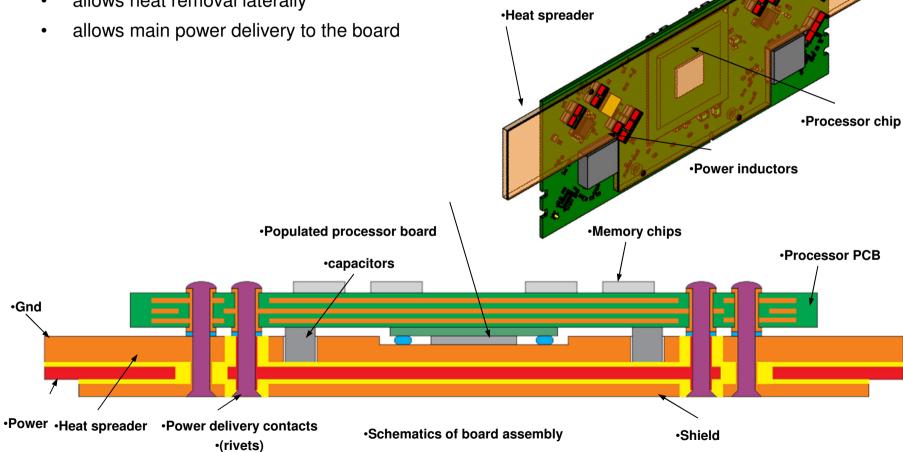
19" 2U Chassis with Combined Cooling and Power



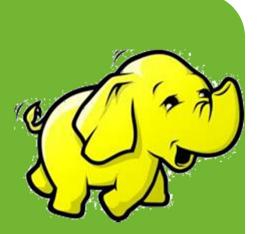
Compute node heat spreader

Functions:

- Electrically and thermally connects the compute node to ٠ cooling-power delivery infrastructure
- allows heat removal laterally ٠







And now the Software story...















64 bit Fedora 17 on P5020DS

- Freescale took kernel version 3.0.34 from kernel.org
- •Configured and compiled it for P5020
- •Took Fedora user space root FS (thru another PPC platform)
- •Runs 100% OK YUM, Gnome desktop, networking, apache, etc...
 - System up and running > 40 days
 - Java, Python, ...
- •This effort took approximately ONE day



IBM DB2 installation on P5020

- Simple install of IBM XL C/C++ runtime (XLC compiler runtime)
- Install libaio
- Simple install of IBM DB2 (express-C, v10.1)
- Some minor configuration adjustments required
- Entire process only took a few hours -- no compilation was needed
- Demo available
 - Technology explorer (runs php in browser)
 - WMD Workload Multi-User Driver (Java based)
 - DB2 data base engine
- Runs stable able to exercise without any issues





Hadoop install on P5020

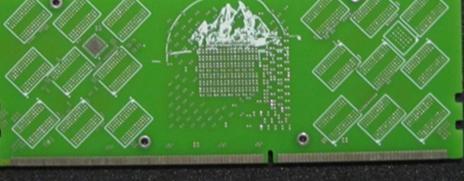
- Simple install (version 1.0.3 for ppc64)
- Minor configuration effort required
- Works for single node and pseudo-distributed mode
- No compilation necessary
- Demo available





Comparison to Calxeda node board





Comparison to Moonshot node board









Acknowledgements

This work is the results of many people

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- Hillery Hunter, IBM WRL
- Vipin Patel, IBM Fishkill
- And many more remain unnamed....







PS. I like lightweight things µServer website: www.swissdutch.ch